

3726

Patent

Docket No.: CDST-C169.CIP

Information Disclosure Statement Transmittal

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner of Patents and Trademarks, Washington, D.C., 20231, on the below date of deposit.					
Date of Deposit:	4-14-03	Name of Person Making the Deposit:	Luz Castillo	Signature of the Person Making the Deposit:	<i>Luz Castillo</i>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Fahlen

Serial No.: 09/993,740

Group Art Unit:

Filed: November 21, 2001

Examiner: To be assigned.

Title: METHOD OF PATTERNING WALL PHOSPHOR WELL MATRIX UTILIZING GLASS

The Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Information Disclosure Statement Transmittal

Transmitted herewith is the following:

- Formal drawings, totaling sheets.
- Informal drawings, totaling sheets.
- ☒ Certification for PTO Consideration
- ☒ Information Disclosure statement (2 sheets)
- Information Disclosure statement and late filing fee
- ☒ Form 1449
- Petition for Extension of Time
- Other:

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Fee Calculation (for other than a small entity)

Fee Items	Fee Rate	Total
Petition for Extension of Time (fee calculated elsewhere)	\$.00	
Information Disclosure Statement, late filing	\$180.00	
Other:		
Total Fees		0.00

PAYMENT OF FEES

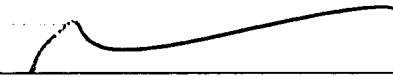
1. The full fee due in connection with this communication is provided as follows:
 - [] The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.
A duplicate copy of this authorization is enclosed.
 - [] A check in the amount of \$
 - [X] Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

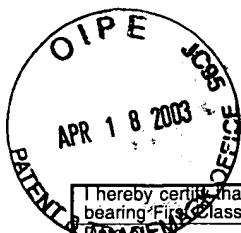
Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

Date: 4/14/07

By: 
John P. Wagner, Jr.
Reg. No. 35, 398



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

UN
#7/IDS
4-24-03

Docket No.: CDST-C169.CIP

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Assistant Commissioner for Patents, Washington, D.C., 20231, on the below date of deposit.			
Deposit:	4-14-03	Name of Person Making the Deposit:	Luz Castille
		Signature of the Person Making the Deposit:	[Signature]

Inventor(s): Fahlen

Serial No.: 09/993,740

Group Art Unit:

Filed: November 21, 2001

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Title: METHOD OF PATTERNING WALL PHOSPHOR WELL MATRIX UTILIZING GLASS

The Assistant Commissioner for Patents
Washington, D.C. 20231
Sir:

Certification for PTO Consideration of an Information Disclosure Statement
(Under 37 CFR §1.97)

Consideration of the enclosed Information Disclosure Statement is requested.

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1. This certification is being made for this information disclosure statement

- ☒ [X] accompanying this certification
☐ [] Filed _____

2. I hereby certify that:

- ☒ X Each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the statement.
☐ No item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart application, or, to the knowledge of the person signing certification after making reasonable inquiry, was known to any individual designated in §1.56(c) more than three months prior to the filing of the statement.

3. The person making this certification is

- ☒ [x] a person who is substantively involved in the preparation or prosecution of the application, and who is associated with the inventor, with the assignee, or with anyone to whom there is an obligation to assign the application (37 C.F.R. 1.56 (c)) and who signs below.
- ☐ [] the inventor(s) who signs below
- ☐ [] the practitioner who signs below on the basis of the information:
☐ [] supplied by the inventors
☐ [] supplied by an individual designated in § 1.56(c)
☐ [] in the practitioners file



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San Jose, California 95113
(408) 938-9060

Respectfully submitted,

Date: _____

4/14/03

By: _____

John P. Wagner, Jr.
Reg. No. 35,398

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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: CDST-C16A-CIP

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Inventor(s): Fahlen
Serial No.: 09/993,740
Group Art Unit:
Filed: November 21, 2001
Examiner: To be assigned.
Title: METHOD OF PATTERNING WALL PHOSPHOR WELL MATRIX UTILIZING GLASS

The Commissioner of Patents and Trademarks
Washington, D.C. 20231
Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:


<u>Pat. No.</u>	<u>Pat. Title</u>	<u>Grant Date</u>
5,372,973	METHOD TO FORM SELF-ALIGNED GATE STRUCTURE AROUND COLD CATHODE EMITTER TIPS USING CHEMICAL MECHANICAL POLISHING TECHNOLOGY	12/13/94
5,445,550	LATERAL FIELD EMITTER DEVICE AND METHOD OF MANUFACTURING SAME	8/29/95
5,492,234	METHOD FOR FABRICATING SPACER SUPPORT STRUCTURES USEFUL IN FLAT PANEL DISPLAYS	2/20/96
5,704,820	METHOD FOR MAKING IMPROVED PILLAR STRUCTURE FOR FIELD EMISSION DEVICES	
5,916,004	PHOTOLITHOGRAPHICALLY PRODUCED FLAT PANEL DISPLAY SURFACE PLATE SUPPORT	6/29/99
5,371,433	FLAT ELECTRON DISPLAY DEVICE WITH SPACER AND METHOD OF MAKING	12/6/94
6,486,010 B1	METHOD FOR MANUFACTURING THIS FILM TRANSISTOR PANEL	11/26/02
6,489,718 b1	SPACER SUITABLE FOR USE IN FLAT PANEL DISPLAY	12/3/02

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